1. Features

- 60 Watt Output Power
- Very small size
- High efficiency DC/DC converter
- Wide adjustable output voltage range
- 1500V isolation (input to output)
- Input voltage range 36V to 57V
- IEEE802.3at compliant
- Low output ripple and noise
- Minimal (low cost) external components required
- Overload, thermal and short-circuit protection
- Silvertel “design-in” assistance

2. Description

The Ag5510 is a High Power over Ethernet (PoE Ultra) module that can deliver up to 60 Watts of output power. Suitable for applications such as WiMAX access points, PTZ cameras, notebook computers and thin client terminals.

The Ag5510 has been designed to extract power from Power Sourcing Equipment (PSE) over an Ethernet cable. The module’s input conforms to the IEEE 802.3at standard for signature recognition and class programming. It has been designed to work with existing high power PSE’s from leading manufacturers.

The high efficiency DC/DC converter operates over a wide input voltage range and provides a regulated low ripple and low noise output. The DC/DC converter also has built-in overload, thermal and short-circuit output protection.
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3. Ag5510 Product Selector

<table>
<thead>
<tr>
<th>Part Number†</th>
<th>Nominal Output Voltage ‡</th>
<th>Maximum Output Power *</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ag5510</td>
<td>12V / 24V</td>
<td>60W</td>
</tr>
</tbody>
</table>

*At 25°C must include adequate thermal management
‡ Voltage depending on output configuration (Parallel or Series), see Section 5.7.

Table 1: Ordering Information

Figure 1: Block Diagram

Figure 2: Ag5510 Package Format
4. Pin Description

**Input Connector Pins**

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VIN+</td>
<td><strong>Direct Input +.</strong> This pin connects to the positive (+) output of the input bridge rectifier.</td>
</tr>
<tr>
<td>2</td>
<td>VIN-</td>
<td><strong>Direct Input -.</strong> This pin connects to the negative (-) output of the input bridge rectifier.</td>
</tr>
<tr>
<td>3</td>
<td>CPA</td>
<td><strong>Class Programming.</strong> The classification current can be changed by connecting an external resistor from this pin to VIN- (see section 5.4). With no resistor fitted this input will default to Class 4.</td>
</tr>
<tr>
<td>4</td>
<td>IC</td>
<td><strong>Internal Connection.</strong> Do not connect to this pin.</td>
</tr>
<tr>
<td>5</td>
<td>SA1</td>
<td>Signature resistance adjustment (see section 5.2)</td>
</tr>
<tr>
<td>6</td>
<td>SA2</td>
<td>Signature resistance adjustment (see section 5.2)</td>
</tr>
</tbody>
</table>

**Output Connector Pins**

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>7 &amp; 8</td>
<td>VOUT 1</td>
<td><strong>DC Output.</strong> This pin provides the main regulated output from the DC/DC converter.</td>
</tr>
<tr>
<td>9</td>
<td>0V 1</td>
<td><strong>Ground.</strong> The ground return for the VOUT 1 output.</td>
</tr>
<tr>
<td>10</td>
<td>VOUT 2</td>
<td><strong>DC Output.</strong> This pin provides the secondary output from the DC/DC converter.</td>
</tr>
<tr>
<td>11 &amp; 12</td>
<td>0V 2</td>
<td><strong>Ground.</strong> The ground return for the VOUT 2 output.</td>
</tr>
<tr>
<td>13</td>
<td>ADJ</td>
<td><strong>Output Adjust.</strong> The output voltage can be adjusted from its nominal value, by connecting an external resistor from this pin to either the VOUT 1 pin or the 0V 1 pin.</td>
</tr>
</tbody>
</table>
5. Functional Description

5.1 Inputs

The Ag5510 has a single input that can be connected to two external bridge rectifiers (see Figure 1: Block Diagram). This allows the Ag5510 to be compatible with equipment that uses the different power options. The Ag5510 was developed to be powered with the Silvertel Ag6110 PSE but can also be used with high Power PSE’s from most leading manufactures such as Microsemi, Phihong, Cisco (uPOE).

![Figure 3: Typical System Diagram](image)

5.2 PD Signature

The Ag5510 input complies with the IEEE802.3at specification. When the inputs are connected to a Power Sourcing Equipment (PSE), they will automatically present a Powered Device (PD) signature to the PSE (when requested). The equipment will then recognise that a PD is connected to that line and supply power.

Note: The SA1 and SA2 pins must be connected together when used with a Phihong POE80U or POE60U PSE.
### 5.3 Isolation

To meet the isolation requirements of IEEE802.3at section 33.4.1 a Powered Device (PD) must pass the electrical strength test of IEC 60950 sub clause 6.2. This calls for either a) 1500VAC test or b) 1500V impulse test. The Ag5510 has been designed to meet b) 1500V impulse test. When mounting the module to the heat sink, you must ensure a clearance of 1.5mm minimum between the pins of the connectors and the heat sink or use some form of isolation such as at least two layers of 3M#56 insulating tape.

Figure 4 shows the position of the isolation barrier on the bottom of the Ag5510.

![Figure 4: Position of the Isolation Barrier](image)

### 5.4 Power Classification

The Ag5510 classification is fixed at Class 4, this means that an IEEE802.3at Type 1 or an IEEE802.3af PSE will default to Class 0. However an IEEE802.3at PSE will recognise the Class 4 as a Type 2 PD. The Ag5510 allows the class current to be externally programmed to Class 5 by connecting a 360Ohm resistor (RC) between the CP and VIN- pins, see below.

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### Table 2: Class Programming

<table>
<thead>
<tr>
<th>CLASS</th>
<th>Programming Resistance (Ohms)</th>
<th>PSE Power (W)</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>Open circuit</td>
<td>up to 60W</td>
</tr>
<tr>
<td>5</td>
<td>360 ±1%</td>
<td>up to 60W</td>
</tr>
</tbody>
</table>

Note: The Ag610 will supply 60W when it sees Class 4 or Class 5.

![Figure 5: Class Programming Option](image)
5.5 High Power Applications

Figures 6a and 6b show typical application diagrams for the Ag5510 connected to the Ag6110 PSE.

Figure 6a – Typical Application Diagram for Ag6110 using 10/100BASE-T

Figure 6b – Typical Application Diagram for Ag6110 using 1000BASE-T

Figure 6: High Power configuration
5.6 DC/DC Converter

The Ag5510’s DC/DC converter provides a regulated low ripple and low noise output that has built-in output over-load and short-circuit protection.

5.7 Output Configuration

The Ag5510 has two outputs which must be connected either in parallel to provide 12V or in series to provide 24V, as shown in Figure 7: Output Configurations.

Output 2 voltage (VOUT 2) tracks the output 1 voltage (VOUT 1). The use of the two outputs separately is not recommended due to voltage regulation issues.

It is important that C1, C3 and C2, C4 are both used and connected as close to the output pins of the Ag5510 as possible (for both configurations).

![Figure 7: Output Configurations](image-url)

5.8 Output Adjustment

The Ag5510 has an ADJ pin, which allows the output voltage to be increased or decreased from its nominal value.

The adjustment range allows the Ag5510 to provide an output voltage from 10.55V up to 30.4V. Voltage must not be adjusted to less than 10.5V or permanent damage may be caused. Contact SilverTel for further details.
Reducing the output voltage from nominal

Increasing the output voltage from nominal

**Figure 8: Output Adjustment**

**Table 3: Output Adjustment Resistor (R) Value**

<table>
<thead>
<tr>
<th>Value of RA1</th>
<th>VOUT Parallel</th>
<th>VOUT Series</th>
</tr>
</thead>
<tbody>
<tr>
<td>Open Circuit</td>
<td>12V</td>
<td>24V</td>
</tr>
<tr>
<td>91K *</td>
<td>10.55V</td>
<td>21.1V</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Value of RA2</th>
<th>VOUT Parallel</th>
<th>VOUT Series</th>
</tr>
</thead>
<tbody>
<tr>
<td>Open Circuit</td>
<td>12V</td>
<td>24V</td>
</tr>
<tr>
<td>8K2</td>
<td>15V</td>
<td>30.2V</td>
</tr>
</tbody>
</table>

* RA1 must not be less than 91K. Adjusting Vout to less than 10.5V may cause permanent damage

**5.9 Output Power**

The maximum output power of the Ag5510 is 60W*; however this is limited by the available input power to the module.

When calculating the output power, the following factors must be taken into account:

1. Ag5510 efficiency
2. PSE output power
3. Cable and connector losses
4. Input bridge rectifier losses

* When the output voltage is adjusted to its maximum the power must be limited to 60W. e.g. with Vout set to 30V the output current must be reduced to 2A.
5.10 Typical Connections

As shown in Figure 9: Typical Connection Diagram, a minimum of 1000μF must be connected across each output, positioned as close to the output pins as possible. These capacitors are needed for output filtering and step load change performance and can be a standard low cost electrolytic; they do not need to be a low ESR type.

The Class programming and the Output Adjust inputs are optional and are provided to give greater flexibility to the Ag5510. Further information on using these inputs can be found in sections 5.4 Power Classification and 5.8 Output Adjustment.

5.11 Additional Output Filtering

The Ag5510 output ripple and noise can be reduced by adding an additional output filter. Figure 10 shows a cost effective method which can reduce the output ripple from 294mVp-p down to 15.6mVp-p when configured in parallel. When configured in series the output ripple can be reduced from 600mVp-p down to 40mVp-p.
**Figure 10: Additional Output Filtering**

- **Ag5510**
  - **VIN+**, **VIN-**, **C1**, **C2**, **C3**, **C4**, **C5**, **C6**, **L1**
  - **VOUT 1**, **0V 1**, **ADJ**, **VOUT 2**, **0V 2**

- Component Details:
  - C1, C2: 1000uF
  - C3, C4: 10uF
  - C5, C6: 10uF
  - L1: 2.2uH

- Additional Output Configuration:
  - 24V Load connected to VOUT 2
6. Operating Temperature Range

It is important to remember that Ag5510 is a power supply, and as such careful consideration should be taken over the mechanical design of the host product, with provision for heat sinking and/or forced air cooling.

At full power the Ag5510 will generate heat. The device has been designed to be used with a heatsink which thermally connects to the Ag5510 through the use of thermal pad such as Bergquist .1” Ultra soft gap pad or thermal paste such as Thermally conductive Oxime Cure paste. The customers PCB can then be thermally mounted (using a Bergquist 0.1” Ultra soft gap pad) to the chassis of the host equipment. Further thermal relief can be in the form of cooled or forced air improving the power output.

Because each application is different it is impossible to give fixed and absolute thermal recommendations. However to obtain maximum power it is important that any enclosure used has sufficient ventilation and airflow over the Ag5510.

Figure 11 shows the maximum ambient temperature under continuous load conditions. The Ag5510 is capable of handling 60W up to 70°C only when mounted to a heatsink, which a suggested mounting method is shown in Figure 14.
If the Ag5510 is thermally connected to the PCB only without an additional heatsink, then 60W can only be obtained up to 50°C. Suggested layout and dimensions of the thermal relief pads can be found in Figure 15 and Figure 16 respectively.

![Graph: Operating Temperature (Thermally connected to PCB)]

When intended for use in ambient temperatures below 0°C we would recommend a low ESR electrolytic capacitor be used on the DC output. Capacitors rated for -55°C operation should be used below 0°C.

7. Protection

The Ag5510 must be protected from over-voltages exceeding the 80V maximum rated surge input voltage. An inexpensive but effective solution can be achieved by connecting a Tranzorb diode across the input; see Apps Note “ANX-POE-Protection”.

8. EMC

The Ag5510 has been designed to pass EN55032 Class B, however the Ag5510 will only be one component within a system so we would always advise that provisions are put in place in case further noise reductions are needed. From our extensive experience we would recommend an inexpensive but effective solution to reduce emissions shown in Figure 13.
9. Electrical Characteristics

9.1 Absolute Maximum Ratings¹

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Min</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 DC Supply Voltage</td>
<td>$V_{CC}$</td>
<td>-0.3</td>
<td>60</td>
<td>V</td>
</tr>
<tr>
<td>2 DC Supply Voltage Surge for 1ms</td>
<td>$V_{SURGE}$</td>
<td>-0.6</td>
<td>80</td>
<td>V</td>
</tr>
<tr>
<td>3 Storage Temperature</td>
<td>$T_S$</td>
<td>-40</td>
<td>+100</td>
<td>°C</td>
</tr>
</tbody>
</table>

Note 1: Exceeding the above ratings may cause permanent damage to the product. Functional operation under these conditions is not implied. Maximum ratings assume free airflow.

*Figure 13: EMC Filtering*
9.2 Recommended Operating Conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 Input Supply Voltage</td>
<td>$V_{IN}$</td>
<td>52</td>
<td>57</td>
<td>36</td>
<td>V</td>
</tr>
<tr>
<td>60W Output Minimum Load</td>
<td>$-12V_{out}$</td>
<td>57</td>
<td>57</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>2 Under Voltage Lockout</td>
<td>$V_{LOCK}$</td>
<td>30</td>
<td>36</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>3 Operating Temperature</td>
<td>$T_{OP}$</td>
<td>-20</td>
<td>25</td>
<td>70</td>
<td>Ta / °C</td>
</tr>
</tbody>
</table>

Note 1: Under Voltage Lockout | $V_{LOCK}$ | 30 | 36 | V |

Note 2: See Section operating temperature range

9.3 DC Electrical Characteristics

<table>
<thead>
<tr>
<th>DC Characteristic</th>
<th>Sym</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
<th>Test Comments Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 Nominal Output Voltage</td>
<td>$+V_{DC}$</td>
<td>11.4</td>
<td>12</td>
<td>12.6</td>
<td>V</td>
<td>Parallel O/P Series O/P</td>
</tr>
<tr>
<td>2 Voltage Adjust Range</td>
<td>$V_{ADJ}$</td>
<td>10.55</td>
<td>30.2</td>
<td>V</td>
<td>See 5.8 Output Adjustment</td>
<td></td>
</tr>
<tr>
<td>3 Continuous Output Current</td>
<td>$I_{CONT}$</td>
<td>5.0</td>
<td>2.5</td>
<td>A</td>
<td>Parallel O/P Series O/P</td>
<td></td>
</tr>
<tr>
<td>4 Line Regulation</td>
<td>$V_{LINE}$</td>
<td>0.1</td>
<td>%</td>
<td>@ 60W Load</td>
<td></td>
<td></td>
</tr>
<tr>
<td>5 Load Regulation</td>
<td>$V_{LOAD}$</td>
<td>0.1</td>
<td>%</td>
<td>@ $V_{IN}=52V$</td>
<td></td>
<td></td>
</tr>
<tr>
<td>6 Output Ripple and Noise</td>
<td>$V_{RN}$</td>
<td>15.6</td>
<td>15.6</td>
<td>mVp-p</td>
<td>@ Max load</td>
<td></td>
</tr>
<tr>
<td>Parallel O/P (12v) Series O/P (24v)</td>
<td>15.6</td>
<td>15.6</td>
<td>mVp-p</td>
<td>@ Max load</td>
<td></td>
<td></td>
</tr>
<tr>
<td>7 Minimum Load</td>
<td>$I_{MIN}$</td>
<td>0</td>
<td>mA</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>8 Short-Circuit Duration</td>
<td>$T_{SC}$</td>
<td>∞</td>
<td>sec</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>9 Efficiency</td>
<td>Eff</td>
<td>89</td>
<td>%</td>
<td>Vin = 52V @1.8A Load</td>
<td></td>
<td></td>
</tr>
<tr>
<td>10 Isolation Voltage (I/O)</td>
<td>$V_{ISO}$</td>
<td>1500</td>
<td>$V_{PK}$</td>
<td>Impulse Test</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Note 1: Typical figures are at 25°C with a nominal 56V supply, parallel output configuration (unless otherwise stated) and are for design aid only. Not Guaranteed

2: The output must not exceed 60W or 5.0A Parallel / 2.5A Series.

3: The output ripple and noise was measured using the Pi Filter shown in figure 9.

4: The Ag5510 has been designed to work normally when no load is connected.
10. Package

(Recommended PCB hole diameter = 1.1 ± 0.05)
Dimensions (in mm) are nominal +/- 0.25 unless otherwise stated
10.1 Heat Sink Assembly

Figure 14: Suggested Heat Sink Assembly
10.2 PCB Thermal Layout

To attain continuous operation at 60W the Ag5510 must be thermally connected to the customers PCB and their enclosure or heatsink. The thermal layout should have copper pads with a via array to help draw the heat through the PCB to the heatsink. An example of a thermal layout can be found below in Figure 15 with dimensions shown in Figure 16.

Figure 15: Suggested PCB Thermal Layout
10.3 Heatpad Footprint

The latest revision of all application notes referenced in this document can be found on the Silver Telecom website “www.silvertel.com”.

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