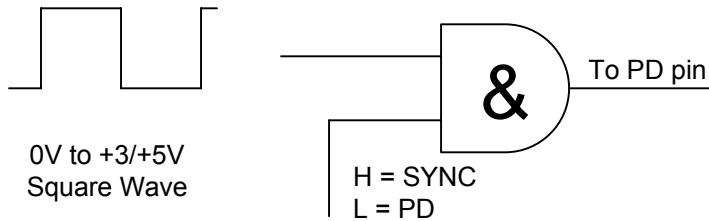


Power Down and Synchronisation of Ag1170

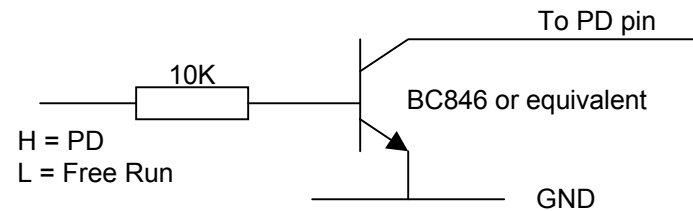


The internal oscillator will “free run” when the PD pin is left open-circuit.

SYNCHRONISATION OR POWER DOWN



POWER DOWN ONLY



The PD input must be: 1) Square wave, or 2) Logic low, or 3) Open circuit.

It must NEVER be logic high – this will damage the DC-DC Converter.

SYNCHRONISATION: If multiple Ag1170 modules are used on the same board, it is recommended that the Ag1170 oscillators are “synchronised” by the use of an external clock. To do this input a common square wave signal to all the PD pins of the Ag1170 devices. Use a 128kHz clock frequency for +5V variants or a 64kHz clock frequency for +3.3V variants. Use a 50:50 Mark to Space ratio.

POWER DOWN: The DC-DC Converter can be switched off by applying a logic low to the PD pin.