

# **Evaluation Board User Manual**



# EvalAg59800-LPB Evaluation Board User Manual

Version 1.1 - July 2025

V1.1 July 2025

# EvalAg59800-LPB

100W High Efficiency Poe++ Module Evaluation Board

## **Table of Contents**

1	Kit	t Contents	3
2	Во	pard Layout	3
	2.1	Link Settings	3
	2.2	Input Output Connections	3
3	Int	roduction	
4	Inp	out	4
	4.1 ·	Operation	
	4.2	Input Power LED	4
	4.3	PSE Detect	
5	Ou	ıtput	
	5.1	Output Power LED	
	5.2	Output Voltage Adjust	
	5.1	Data Output	5
6	ΕN	M	
7		st Setup	
8	Ad	Iditional information	6
9	Sc	hematic	7
10	Bil	ll of Materials	8
11	La	yer Routing	9
T:	ahla	e of Figures	
			_
		: EvalAg59800-LPB Board Layout	
		2: Basic Test Setup	
		S: Schematic	
		E Bill of Materials	
		: Top Layer Routing	
Fig	ıure 6	: Bottom Layer Routing	9

100W High Efficiency Poe++ Module Evaluation Board

#### 1 Kit Contents

- EvalAg59800-LPB Evaluation Board
- Ag598xx-LPB Module Soldered to Evaluation Board

#### 2 Board Layout

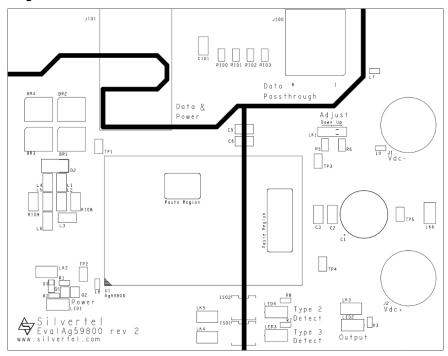


Figure 1: EvalAg59800-LPB Board Layout

#### 2.1 Link Settings

#### 2.2 Input Output Connections

LK1 – Output voltage Adjust	J1 & J2 – Load Output Binding Posts
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LK2 – Input Power LED Enable J101 – PoE Enabled RJ45

LK3 - Output Power LED Enable J100 - Data Passthrough RJ45

LK4 - Type 3 Detect LED Enable

LK5 - Type 2 Detect LED Enable

#### 3 Introduction

This Manual is a guide to using the EvalAg59800-LPB evaluation board fitted with a Silvertel Ag59812-LPB or Ag59824-LPB miniaturised high efficiency Class 8 surface mount Power over Ethernet (PoE) module.

This board has been designed to assist with evaluating the use of Silvertel's module in an application; as such it has been designed to pass through Ethernet data signals (10/100/1000BASE-T) from the Midspan PSE (Power Sourcing Equipment) or PoE enabled switch connected to J101, onto the system connected to J100.

V1.1 July 2025

# EvalAg59800-LPB

100W High Efficiency Poe++ Module **Evaluation Board** 

While this evaluation board has been designed following Silvertel's recommendations, it should not be considered as a reference design as it features circuity included solely for the purposes of evaluation that are not be required for proper operation.

#### 4 Input

The EvalAg59800-LPB board will extract the power from the data pairsets using the RJ45 connector with integrated magnetics, J101. The data is passively passed on to any peripheral equipment via J100, while the power is provided to the Ag59800-LPB module in the correct polarity via the Schottky rectifier diodes BR1, BR2, BR3 and BR4.

#### 4.1 Operation

To ensure that the PSE does not apply power to a non-PoE enabled device the output port first checks for a valid PoE signature. If the PSE does not see a valid signature, then it will disconnect, wait approximately 2 seconds then try again.

Once a valid signature has been detected the PSE may then perform classification to determine the power requirement of the PD, only after this has occurred will the PSE supply power to the powered device.

The module will provide the 25K signature followed by a class 8 power level request to be powered by an IEEE compliant PSE.

If PSE is not capable of suppling the Class 8 power level requested by the Ag59800-LPB, if using an IEEE802.3af or IEEE802.3at PSE for example, it should supply the highest power level it is capable of supplying.

If the PSE does not detect a valid signature on both pairsets of the ethernet cable, the power will be limited to Class 4 power levels.

#### 4.2 Input Power LED

LED1 illuminates when the module is being supplied with greater than 36V. This can be disabled by removing the jumper link LK2, removing this link does not affect the power being delivered to the Ag59800-LPB.

#### 4.3 **PSE Detect**

When the Ag59800-LPB module fitted has detected it is being powered by an IEEE802.3 type 4 (PoE++) or greater PSE, both the 'TYP3-DET' pin and the 'TYP2-DET' pin will be pulled low, and this will cause LED3 and LED 4 to become illuminated on the EvalAg59800-LPB board.

When the Ag59800-LPB module fitted has detected it is being powered by an IEEE802.3 type 3 (PoE++) or greater PSE, the 'TYP3-DET' pin will be pulled low, and this will cause LED3 to become illuminated on the EvalAg59800-LPB board. LED4 will not be illuminated.

When the Ag59800-LPB module fitted has detected it is being powered by an IEEE802.3 type 2 (PoE+) PSE, the 'TYP2-DET' pin will be pulled low, and this will cause LED4 to become illuminated on the EvalAq59800-LPB board. LED3 will not be illuminated.

If an IEEE802.3 Type 1 PSE is detected, neither LED3 nor LED4 with be illuminated.

# EvalAg59800-LPB

V1.1 July 2025

100W High Efficiency Poe++ Module Evaluation Board

#### 5 Output

The Ag59800-LPB output voltage will be present on the two binding post terminations J1 and J2. These can be connected to with 4mm Banana connectors, bare wire, or fork connectors.

#### 5.1 Output Power LED

LED2 illuminates when the module is outputting. This can be disabled by removing the jumper link LK3, removing this link does not affect the power being supplied by the Ag59800-LPB.

#### 5.2 Output Voltage Adjust

The output voltage of the Ag59800-LPB module can be adjusted by changing the location of the jumper LK1. The EvalAg59800-LPB is fitted with two adjust resistors. The down adjust resistor R5, is fitted with a  $68k\Omega$  resistor. And the up adjust resistor R6, which is fitted with a  $0\Omega$  resistor to allow for the maximum adjust change of the Ag59800-LPB module.

To increase the output voltage, insert the jumper to LK1 in the left position, so that the link is between the middle and leftmost pin.

To reduce the output voltage, insert a jumper to LK1 in the right position, so that the link is between the middle and rightmost pin.

If the output voltage needs to be set to a different value (within the adjustment range) then connect different value resistors in place of R5 (68K) or R6 (0 $\Omega$ ) and connect the jumper into the corresponding link setting.

Note: ensure that the down adjust resistor is not shorted when connecting a module that has a higher stated resistance in the datasheet, as this could cause damage to the module.

#### 5.1 Data Output

Any data that is provided over the PI (Ethernet cable) connected to the Data & Power port, J100 will be transposed onto the Data output port, J101 via the data transformer.

The data traces on the evaluation board have been designed to pass through 10/100/1000BASE-T Ethernet data signals. No processing or amplification of this signal will be performed on the evaluation board.

#### 6 EMI

The EvalAg59800-LPB contains the recommended components for EMI filtering, see ANX-POE-EMI for more details.

The board is fitted with ferrite beads, L1-6; if these are not required, they can be effectively removed from the circuit by fitting R108 and R109 with  $0\Omega$  resistors.

#### 7 Test Setup

Figure 2 shows the basic set up using the EvalAg59800-LPB evaluation board powered by Silvertel's EvalAg6810 PSE.

The equipment required: -

- ➤ EvalAg59800-LPB fitted with Silvertel's Ag59812-LPB or Ag59824-LPB PD Module.
- EvalAg6810 or other IEEE802.3 compliant PSE
- ➤ Power supply unit, +52-57V output e.g. 60V bench power supply
- CAT5e or greater cables
- > Application Circuit

Optional equipment: Data source e.g. PC

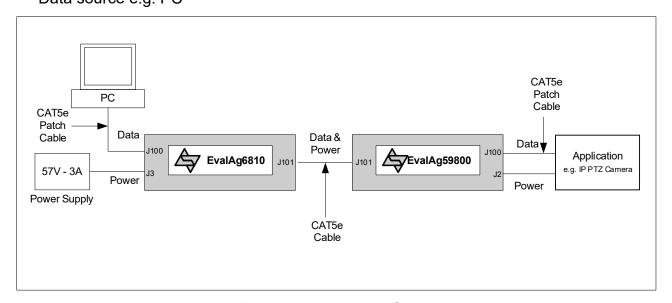


Figure 2: Basic Test Setup

#### 8 Additional information

Full operating conditions and feature set can be found in the Ag59800-LPB product datasheet, available from www.silvertel.com.

100W High Efficiency Poe++ Module Evaluation Board

#### 9 Schematic

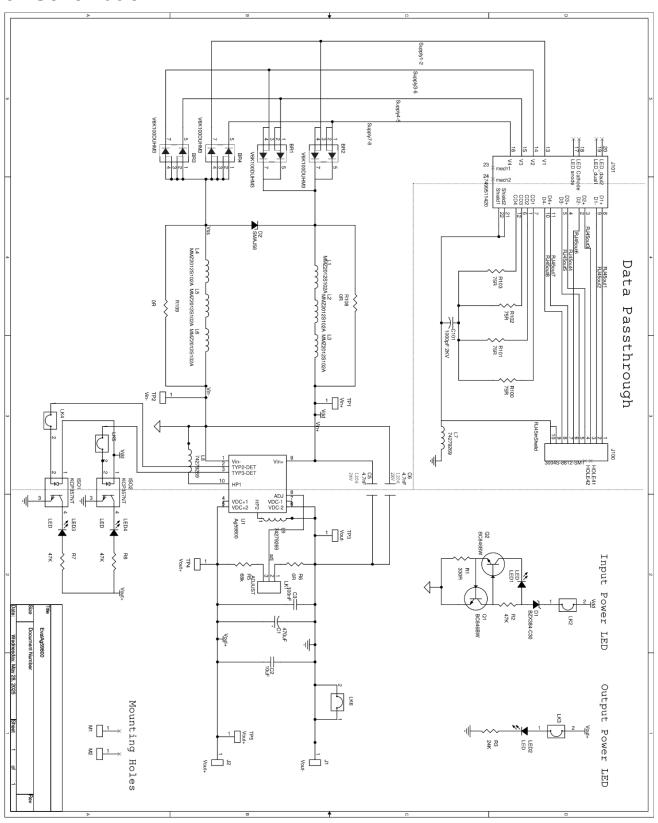


Figure 3: Schematic

100W High Efficiency Poe++ Module Evaluation Board

### 10 Bill of Materials

-					60	Total			
		ŀ			O	LK1-5	Links	Jumper Links	
	Essentra				4	M1, M2, J1, J2	GRF24006A	Feet	
					2	M1, M2	M4 Stud	Standoff	
							Rev.2 - 114.9mm x m90.2mmm	PCB	
	Toby LHCS-03S-R-060-034, Wurth 61300311121	Ŀ		Through Hole		K1	3 Way	Link	
	Toby LHCS-02S-R-060-034, Wurth 61300211121			Through Hole	5	LK2-LK6	2Way	Link	
	Wurth - 7499511420	·		SMT	1	J101	7499511420	Ethernet Connector With Magnetics	
	Toby 3004S-8821-SMT	ŀ	ŀ	SMT	_	J100	RJ45	Ethernet Connector	
	Mac 8	ŀ		SMT	5	TP1-TP5	HK-1-G	Test Point	
	Switch Electronics 354147	Ŀ		Screw Mount	_	J2	Binding Post	Red Connecor	
	Switch Electronics 354146			Screw Mount	_	J1	Binding Post	Black Connector	
	Royal Ohm, Eurohm & Yageo	1%	250mW	1206	0	R108, R109	0R	Resistor - 1206	
	Royal Ohm, Eurohm & Yageo	1%	125mW	0805	4	R100-103	75R	Resistor - 0805	
	Royal Ohm, Eurohm & Yageo	1%	63mW	0603	2	R7, R8	2K	Resistor - 0603	
	Royal Ohm, Eurohm & Yageo	1%	63mW	0805	_	R5	68K	Resistor - 0805	
	Royal Ohm, Eurohm & Yageo	1%	63mW	0805	2	R4, R6	0R	Resistor - 0805	
	Royal Ohm, Eurohm & Yageo	1%	63mW	0603	1	R3	4.7K	Resistor - 0603	
	Royal Ohm, Eurohm & Yageo	1%	63mW	0603	1	R2	47K	Resistor - 0603	
	Royal Ohm, Eurohm & Yageo	1%	63mW	0603	1	R1	330R	Resistor - 0603	
)24	Samsung, NIC, TDK, Murata, Kemet & AVX, Wurth-885342208024	20%	2KV	1206	1	C101	1000pF 2KV	Ceramic multi-layer	
	Multicomp MC1206B472K102CT, Wurth 885342208020	20%	1KV	1206	2	C5, C6	4.7nF	Ceramic multi-layer	
		20%	25V	1206	1	C3	100nF	Ceramic multi-layer	
	Multicomp MC1206F106Z250CT	20%	25V	1206	_	C2	10μF	Ceramic multi-layer	
	Kemet A750MS477M1EAAE015, Wurth 860040475008	20%	25V	Through Hole	_	C1	470µF	Capacitor Electolytic	
	Wurth, Cosmo Electronics Corp			SMT	2	ISO1, ISO2	140356145200, KPC357NT	Opto-Isolator	
	Infineon, ON-Semi , NXP , Diodes Inc.		60V	SOT323	2	Q1, Q2	BC846BW	NPN Transistor	
	Wurth 74279269	25%		0603	3	L7-9	≥1000Ω@100MHz	Ferrite Chip	
	Bourns MH3261-601Y, Wurth 74279218	25%	≥ 2A	1206	6	L1-6	≥600Ω@100MHz	Ferrite Chip	
	Wurth - 150 141 RS7 310 0			SMT	4	LED1-LED4	RED LED	SMD LED	
	Vishay			FlatPAK 5x6	4	BR1-BR4	V6K100DUHM3	Bridge Rectifiers	
	Wurth- 824 500 581, Vishay, ST Micro, Diodes Inc			SMA	1	D2	SMAJ58A	Protection Diode	
	Vishay		-	SOD323	1	D1	BZX384-C30	Zener Diode	
	Silver Telecom Part			Custom	1	U1	Ag59800-LPB	PD Module	
		1			İ				
	Supplier Pt NO:	<u>ii</u>	Rating:	Package:	Qty:	Location:	<u>Value</u>	Description	Silver
	Date: 8th July, 2025			- Rev.1.1	Board	EvalAg59800-LPB Eval Board - Rev.1.1	Eval		

Figure 4: Bill of Materials

# 11 Layer Routing

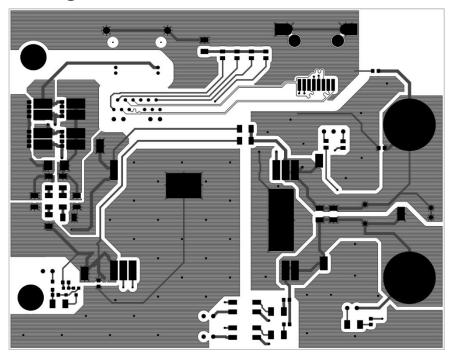


Figure 5: Top Layer Routing

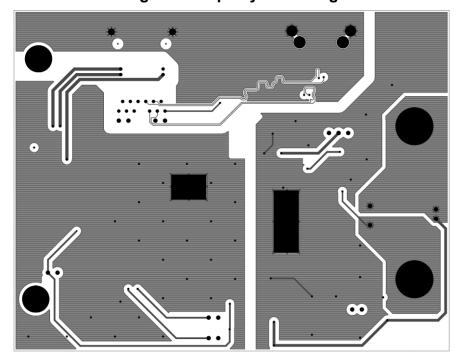


Figure 6: Bottom Layer Routing