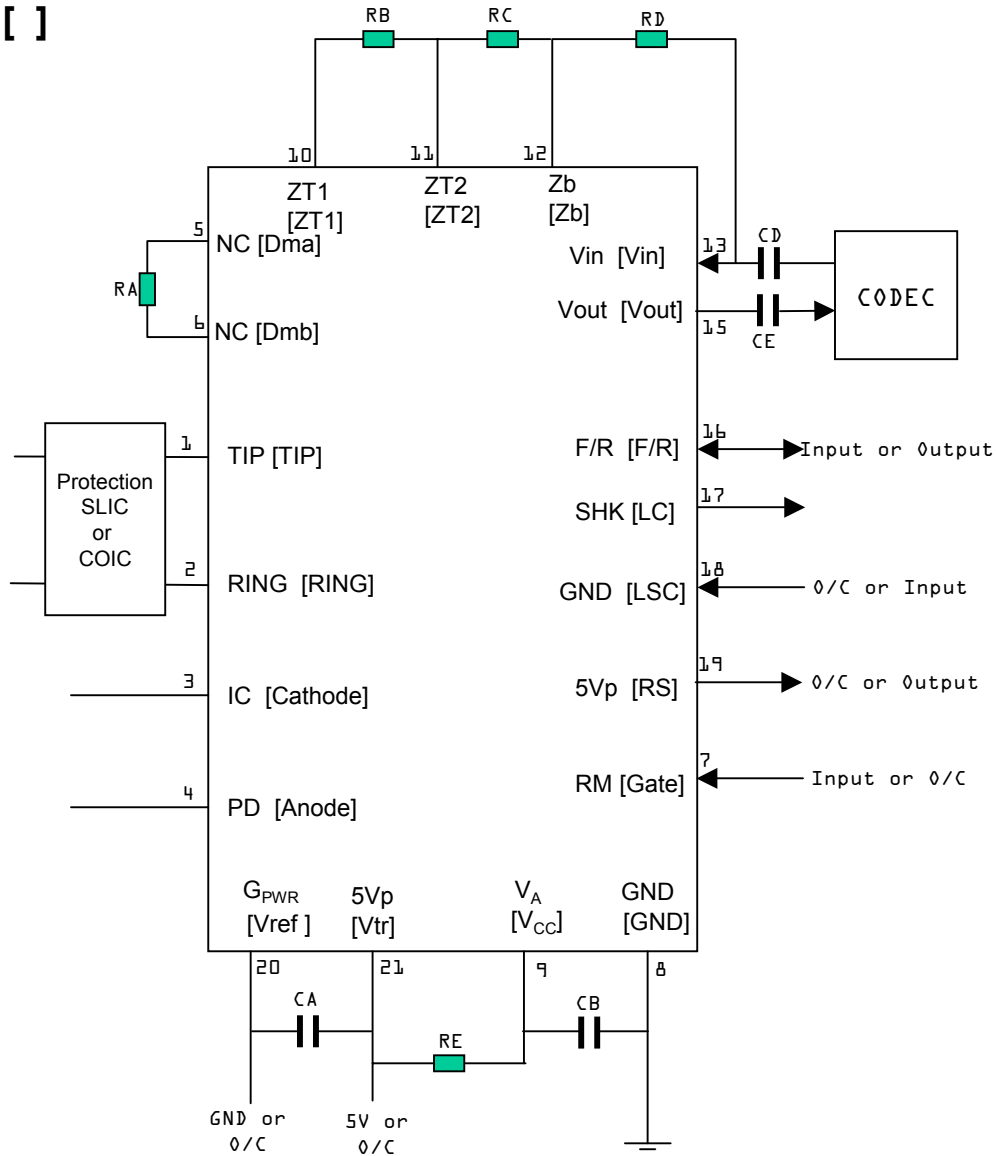


Dual Layout of Ag2120D & Ag1170D

COIC connections are shown in brackets []

Pin Outs	
1170D SLIC	2120D COIC
TIP	[TIP]
RING	[RING]
IC (NC)	[Cathode]
PD	[Anode]
NC	[Dma]
NC	[Dmb]
RM	[Gate]
GND _A	[GND _A]
V _A	[Vcc]
ZT ₂	[ZT ₂]
ZT ₁	[ZT ₁]
Zb	[Zb]
Vin	[Vin]
NC	[NC]
Vout	[Vout]
F/R	[F/R]
SHK	[LC]
G _{PWR}	[LSC]
5Vp	[RS]
G _{PWR}	[Vref]
5Vp	[Vtr]



Notes

- For COIC, Don't drive Pin 3.
- For SLIC, Don't fit RB/RC/RD.
- SLIC and COIC will require different protection circuits.
- For COIC, Don't fit RE or CA.
- COIC off-hook power protection not used.
- COIC parallel phone detection not used
- PD left O/C (may need to connect this).
- Values for R's and C's are shown in datasheet.